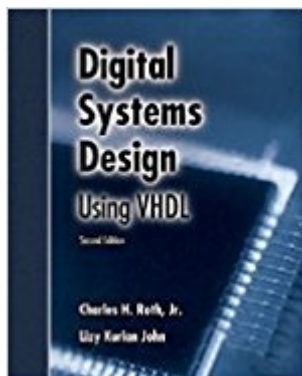


The book was found

Digital Systems Design Using VHDL



Synopsis

Written for an advanced-level course in digital systems design, **DIGITAL SYSTEMS DESIGN USING VHDL** integrates the use of the industry-standard hardware description language VHDL into the digital design process. Following a review of basic concepts of logic design, the author introduces the basics of VHDL, and then incorporates more coverage of advanced VHDL topics. Rather than simply teach VHDL as a programming language, this book emphasizes the practical use of VHDL in the digital design process.

Book Information

Hardcover: 592 pages

Publisher: CL Engineering; 2 edition (March 30, 2007)

Language: English

ISBN-10: 0534384625

ISBN-13: 978-0534384623

Product Dimensions: 8.3 x 1.2 x 9.3 inches

Shipping Weight: 2.5 pounds (View shipping rates and policies)

Average Customer Review: 3.9 out of 5 stars 12 customer reviews

Best Sellers Rank: #126,181 in Books (See Top 100 in Books) #57 in [Books > Engineering &](#)

[Transportation > Engineering > Electrical & Electronics > Digital Design](#) #66 in [Books >](#)

[Computers & Technology > Computer Science > Systems Analysis & Design](#) #614 in [Books >](#)

[Textbooks > Computer Science > Programming Languages](#)

Customer Reviews

Charles Roth is Professor Emeritus in Electrical and Computer Engineering at the University of Texas at Austin, where he taught Digital Design for more than four decades. In addition to co-authoring **DIGITAL SYSTEMS DESIGN USING VHDL**, Dr. Roth has authored the successful **FUNDAMENTALS OF LOGIC DESIGN** and co-authored **DIGITAL SYSTEMS DESIGN USING VERILOG**. Lizy John is the B. N. Gafford Professor in Electrical and Computer Engineering at the University of Texas at Austin. Dr. John has been teaching and conducting research in computer architecture and digital systems design for almost two decades. She has coauthored **DIGITAL SYSTEMS DESIGN USING VHDL** and **DIGITAL SYSTEMS DESIGN USING VERILOG** and has edited several successful books on computer performance evaluation and workload characterization. She is an IEEE Fellow.

This book was required for a class and ended up being a great resource for learning VHDL. I only rented it because of the price and was disappointed to have to send it back.

Great book for learning VHDL and to use for reference. Will definitely keep in my library. I don't think this is the best material for advanced users as some of my assignments require me to look elsewhere.

Book was delayed by weather but came brand new in package like description

A lot of "examples" in this text need to be updated, as the coding in more current IDEs don't use that specific terminology anymore. As for general understanding, it's alright, clear and lots of examples, but as before, if you're using a more current IDE software (like Xilinx or similar) a lot of the coding is incorrect and doesn't translate to the program, but the structure of the coding does. Just be careful if you're trying to implement what you're learning on your own.

This was a book required by a VHDL class I took while completing my undergrad degree. I only ever opened it to look at the homework questions. I never had to read it because I had a good professor, but when skimming through the chapters, it didn't seem like any of the ideas/concepts were presented in a way as clear as the professor made them.

Quite honestly, a PDF of this book with quick search would have been of better use to modern EE and ECE students.

Well I the book is not in an awful condition. But I was looking for books that were in really good condition and this has some pages that have a lot of things written down. And the CD is missing which apparently has a lot of useful information

I bought this book and it was recommended as a textbook for the VHDL course. I have this book and I am 100% sure that this book is NOT one of the good VHDL books you need/want in your library. It belongs to the "OK" level. chapter one: is the most valuable chapter. it contains an excellent review for every thing in digital logic, and guess what? chapter two: is a poor organized chapter that presents VHDL basics. after reading this chapter you will stop reading because you will start searching for other well organized books! that was my situation and 9 of my friends got similar

situation. after reading 3 books in VHDL, I highly recommend: 1- only the first chapter of this book 2- Circuit Design with VHDL by Pedroni 3- some examples from "Design Recipes for FPGAs" by Wilson

[Download to continue reading...](#)

Digital Design Using VHDL: A Systems Approach Digital Systems Design Using VHDL Advanced Digital Logic Design Using VHDL, State Machines, and Synthesis for FPGA's Digital Design with RTL Design, VHDL, and Verilog Digital Logic and Microprocessor Design with VHDL Fundamentals of Digital Logic with VHDL Design Digital Design with CPLD Applications and VHDL RTL Hardware Design Using VHDL: Coding for Efficiency, Portability, and Scalability Design Recipes for FPGAs, Second Edition: Using Verilog and VHDL Digital Electronics: A Practical Approach with VHDL (9th Edition) Digital Fundamentals with VHDL Introduction to Embedded Systems: Using ANSI C and the Arduino Development Environment (Synthesis Lectures on Digital Circuits and Systems) Signals and Systems using MATLAB, Second Edition (Signals and Systems Using MATLAB w/ Online Testing) Career Building Through Using Digital Design Tools (Digital Career Building) The Adobe Photoshop Lightroom: 17 Tips You Should Know to Get Started Using Photoshop Lightroom (For Digital Photographers) (Graphic Design, Adobe Photoshop, Digital Photography, Lightroom) Circuit Design and Simulation with VHDL (MIT Press) Introduction to Logic Circuits & Logic Design with VHDL Circuit Design with VHDL Digital Systems Design and Prototyping: Using Field Programmable Logic and Hardware Description Languages Digital Design (Verilog): An Embedded Systems Approach Using Verilog

[Contact Us](#)

[DMCA](#)

[Privacy](#)

[FAQ & Help](#)